

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Stephen E. J. Blightman, et al.

Assignee: Alacritech, Inc.

Title: "NETWORK INTERFACE DEVICE EMPLOYING A DMA
COMMAND QUEUE"

Serial No.: Unknown

Filed: May 14, 2001

Examiner:

Tel:

Docket No.: ALA-016

Art Unit:

Box Patent Application
Assistant Commissioner For Patents
Washington, D.C. 20231

May 14, 2001

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, §1.97 and §1.98, Applicants bring the 73 documents listed on the enclosed forms PTO-1449 to the Examiner's attention in the above-captioned application. Citation of the listed documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant application;
2. a representation that a search has been made; or
3. an admission that the information cited is, or is considered to be, material to patentability as defined in §1.56(b).

Copies of the listed documents on pages 1-4 on the enclosed forms PTO-1449 are not provided because they were either cited by the Examiner or submitted by Applicants in prior applications (09/464,283 and/or 09/067,544) that are relied upon for an earlier filing date under 35 U.S.C. §120.

I hereby certify that this correspondence is being deposited with the United States Postal Service as "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated below and is addressed to: Box Patent Application, Assistant Commissioner for Patents, Washington, D.C. 20231, on May 14, 2001.

May 14, 2001
Date

Lester Wallace
T. Lester Wallace

Respectfully submitted,

Lester Wallace

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Attorney for Applicants
Reg. No. 34,748



U.S. Department of Commerce, Patent and Trademark Office	Application No.: Unknown
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Blightman, et al.
	Group Art Unit: Unknown
"NETWORK INTERFACE DEVICE EMPLOYING A DMA COMMAND QUEUE"	Examiner name: Unknown
Express Mail No.: EF055070213US	Attorney Docket No. ALA-016

U.S. PTO
09/855979



U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	AA	5,280,477	1/18/94	Trapp	370	85.1	
	AB	5,448,566	9/5/95	Richter, et al.	370	94.1	
	AC	5,634,127	5/27/97	Cloud, et al.	395	680	
	AD	5,751,715	5/12/98	Chan, et al.	370	455	
	AE	4,991,133	2/5/91	Davis, et al.	364	900	
	AF	5,163,131	11/10/92	Row, et al.	395	200	
	AG	5,212,778	5/18/93	Dally, et al.	395	400	
	AH	5,289,580	2/22/94	Latif, et al.	395	275	
	AI	5,303,344	4/12/94	Yokoyama, et al.	395	200	
	AJ	5,412,782	5/2/95	Hausman, et al.	395	250	
	AK	5,485,579	1/16/96	Hitz, et al.	395	200.12	
	AL	5,506,966	4/9/96	Ban	395	250	
	AM	5,511,169	4/23/96	Suda	395	280	
	AN	5,548,730	8/20/96	Young, et al.	395	280	

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	AO	PCT/US98/08719 (WO98/50852)	11/12/98	International				
	AP	PCT/US97/17257 (WO98/19412)	5/7/98	International				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AQ	Internet pages entitled: DART Fast Application – Level Networking Via Data-Copy Avoidance, by Robert J. Walsh, printed 6/3/99.
	AR	Internet pages of InterProphet entitled: Frequently Asked Questions, by Lynne Jolitz, printed 6/14/99.
	AS	Internet pages entitled: Technical White Paper – Xpoint's Disk-to-LAN Acceleration Solution for Windows NT Server, printed 6/5/97.

Examiner	Date Considered
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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	BA	5,588,121	12/24/96	Reddin, et al.	395	200.15	
	BB	5,590,328	12/31/96	Seno, et al.	395	675	
	BC	5,592,622	1/7/97	Isfeld, et al.	395	200.02	
	BD	5,634,099	5/27/97	Andrews, et al.	395	200.07	
	BE	5,642,482	6/24/97	Pardillos	395	200.2	
	BF	5,671,355	9/23/97	Collins	395	200.2	
	BG	5,692,130	11/25/97	Shobu, et al.	395	200.12	
	BH	5,699,317	12/16/97	Sartore, et al.	395	230.06	
	BI	5,701,434	12/23/97	Nakagawa	395	484	
	BJ	5,749,095	5/5/98	Hagersten	711	141	
	BK	5,752,078	5/12/98	Delp, et al.	395	827	
	BL	5,758,084	5/26/98	Silverstein, et al.	395	200.58	
	BM	5,758,089	5/26/98	Gentry, et al.	395	200.64	
	BN	5,758,186	5/26/98	Hamilton, et al.	395	831	
	BO	5,758,194	5/26/98	Kuzma	395	886	
	BP	5,790,804	8/4/98	Osborne	395	200.75	
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	BQ	Jato Technologies Internet pages entitled: Network Accelerator Chip Architecture, twelve-slide presentation, printed 8/19/98.					
	BR	EETIMES article entitled: Enterprise System Uses Flexible Spec, by Christopher Harrer and Pauline Shulman, dated August 10, 1998, Issue 1020, printed 11/25/98.					
	BS	Internet pages entitled: iReady About Us and iReady Products, printed 11/25/98.					
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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	CA	5,812,775	9/22/98	Van Seters, et al.	395	200.43	
	CB	5,056,058	10/8/91	Hirata, et al.	364	900	
	CC	5,678,060	10/14/97	Yokoyama et al.	709	212	
	CD						
	CE						
	CF						
	CG						
	CH						
	CI						
	CJ						
	CK						
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	CL	Internet pages entitled: Smart Ethernet Network Interface Card, which Berend Ozceri is developing, printed 11/25/98.					
	CM	Internet pages entitled : Hardware Assisted Protocol Processing, which Eugene Feinberg is working on, printed 11/25/98.					
	CN	Internet pages of XaQti Corporation entitled: Giga POWER Protocol Processor Product Preview, printed 11/25/98.					
	CO	Internet pages of Xpoint Technologies www.xpoint.com web site (5 pages), printed 12/19/97.					
	CP	Internet pages relating to iReady Corporation and the iReady Internet Tuner Module, printed 11/2/98.					
	CQ	Internet pages entitled: Asante and 100BASE-T Fast Ethernet, printed 5/27/97.					
	CR	Internet pages entitled: A Guide to the Paragon XP/S-A7 Supercomputer at Indiana University, printed 12/21/98.					
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	DA	5,566,170	10/15/96	Bakke, et al.	370	60	
	DB	6,009,478	12/28/99	Panner, et al.	710	5	
	DC	6,034,963	3/07/00	Minami, et al.	370	401	
	DD	5,878,225	03/02/99	Bilansky et al.	395	200.57	
	DE	5,991,299	11/23/99	Radogna et al.	370	392	
	DF	5,930,830	07/27/99	Mendelson et al.	711	171	
	DG	6,061,368	05/09/00	Hitzelberger	370	537	
	DH						
	DI						
	DJ						
	DK						
	DL						
	DM						
	DN						

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	DO	PCT/US98/14729 (WO99/04343)	1/28/99	International				
	DP							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	DQ	60/053,240 (U.S. Provisional Application), by Jolitz et al. (listed filing date July 18, 1997).
	DR	Zilog Product Brief entitled "Z85C30 CMOS SCC Serial Communication Controller", Zilog Inc., 3 pages (1997).
	DS	Andrew S. Tanenbaum, Computer Networks, Third Edition, ISBN 0-13-349945-6 (1996).

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	EA	4,336,538	12/28/92	Johnson et al.	364	200	
	EB	5,097,442	03/17/92	Ward et al.	365	78	
	EC	5,629,933	05/13/97	Delp et al.	370	411	
	ED	5,664,114	09/02/97	Krech, Jr. et al.	395	200.64	
	EE	5,794,061	08/11/98	Hansen et al.	395	800.01	
	EF	5,802,580	09/01/98	McAlpice	711	149	
	EG	5,815,646	09/29/98	Purcell et al.	395	163	
	EH	5,771,349	06/23/98	Picazo, Jr. et al.	395	188.01	
	EI						
	EJ						
	EK						
	EL						
	EM						

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	EN	PCT/US98/24943 (WO 00/13091)	11/20/98	International				
	EO							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	EP	Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).
	EQ	VT8501 Apollo MVP4 Documentation, VIA Technologies, Inc., pages i-iv, 1-11, cover and copyright page, revision 1.3 (Feb. 1, 2000).
	ER	Internet pages entitled: Northridge/Southbridge vs. Intel Hub Architecture, 4 pages, printed 2/19/01.
	ES	Gigabit Ethernet Technical Brief, Achieving End-to-End Performance. Alteon Networks, Inc., First Edition, September 1996.
	ET	Internet pages directed to; Technical Brief on Alteon Ethernet Gigabit NIC technology, www.alteon.com, 14 pages, printed 3/15/97.

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